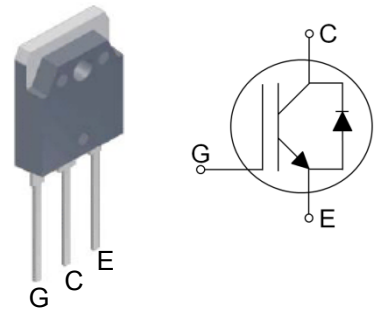


Features

- 600V Fast Field Stop Trench Technology
- Low Switching Loss for a Wide Temperature Range
- Positive Temperature Coefficient
- Easy Parallel Operation
- RoHS Compliant
- JEDEC Qualification



Applications

UPS, Welder, Inverter, Solar

Device	Package	Marking	Remark
TGAN80N60F2DS	TO-3PN	TGAN80N60F2DS	RoHS

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit	
Collector-Emitter Voltage	V_{CES}	600	V	
Gate-Emitter Voltage	V_{GES}	± 20	V	
Continuous Collector Current	I_C	$T_C = 25\text{ }^\circ\text{C}$	160	A
		$T_C = 100\text{ }^\circ\text{C}$	80	A
Pulsed Collector Current (Note 1)	I_{CM}	240	A	
Diode Continuous Forward Current	I_F	40	A	
Power Dissipation	P_D	$T_C = 25\text{ }^\circ\text{C}$	481	W
		$T_C = 100\text{ }^\circ\text{C}$	192	W
Operating Junction Temperature	T_J	-55 ~ 150	$^\circ\text{C}$	
Storage Temperature Range	T_{STG}	-55 ~ 150	$^\circ\text{C}$	
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	300	$^\circ\text{C}$	

Notes :

(1) Repetitive rating : Pulse width limited by maximum junction temperature

Thermal Characteristics

Parameter	Symbol	Value	Unit
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$ (IGBT)	0.26	$^\circ\text{C/W}$
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$ (DIODE)	1.0	$^\circ\text{C/W}$
Maximum Thermal resistance, Junction-to-Ambient	$R_{\theta JA}$	40	$^\circ\text{C/W}$

Electrical Characteristics of the IGBT $T_C=25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test condition	Min.	Typ.	Max.	Unit
OFF						
Collector – Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0V, I_C = 1mA$	600	--	--	V
Zero Gate Voltage Collector Current	I_{CES}	$V_{CE} = 600V, V_{GE} = 0V$	--	--	1	mA
Gate – Emitter Leakage Current	I_{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	--	--	± 250	nA
ON						
Gate – Emitter Threshold Voltage	$V_{GE(TH)}$	$V_{GE} = V_{CE}, I_C = 80mA$	4.5	6.0	7.5	V
Collector – Emitter Saturation Voltage	$V_{CE(SAT)}$	$V_{GE} = 15V, I_C = 80A, T_C = 25^\circ\text{C}$	--	1.8	2.2	V
		$V_{GE} = 15V, I_C = 80A, T_C = 150^\circ\text{C}$	--	2.2	--	V
DYNAMIC						
Input Capacitance	C_{IES}	$V_{CE} = 30V,$ $V_{GE} = 0V$ $f = 1MHz$	--	5140	--	pF
Output Capacitance	C_{OES}		--	225	--	pF
Reverse Transfer Capacitance	C_{RES}		--	140	--	pF
SWITCHING (Note 2)						
Turn-On Delay Time	$t_{d(on)}$	$V_{CC} = 400V, I_C = 80A$ $R_G = 3\Omega, V_{GE} = 15V$ Inductive Load, $T_C = 25^\circ\text{C}$	--	50	--	ns
Rise Time	t_r		--	155	--	ns
Turn-Off Delay Time	$t_{d(off)}$		--	120	--	ns
Fall Time	t_f		--	105	157	ns
Turn-On Switching Loss	E_{ON}		--	4.70	7.05	mJ
Turn-Off Switching Loss	E_{OFF}		--	2.22	3.33	mJ
Total Switching Loss	E_{TS}		--	6.92	10.38	mJ
Turn-On Delay Time	$t_{d(on)}$	$V_{CC} = 400V, I_C = 80A$ $R_G = 3\Omega, V_{GE} = 15V$ Inductive Load, $T_C = 150^\circ\text{C}$	--	55	--	ns
Rise Time	t_r		--	165	--	ns
Turn-Off Delay Time	$t_{d(off)}$		--	130	--	ns
Fall Time	t_f		--	100	--	ns
Turn-On Switching Loss	E_{ON}		--	5.25	7.87	mJ
Turn-Off Switching Loss	E_{OFF}		--	2.45	3.67	mJ
Total Switching Loss	E_{TS}		--	7.70	11.54	mJ
Total Gate Charge	Q_g	$V_{CC} = 400V, I_C = 80A$ $V_{GE} = 15V$	--	220	330	nC
Gate-Emitter Charge	Q_{ge}		--	40	60	nC
Gate-Collector Charge	Q_{gc}		--	110	165	nC

Notes :

(2) Not subject to production test – verified by design/characterization

Electrical Characteristics of the DIODE $T_C=25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test condition	Min.	Typ.	Max.	Unit	
Diode Forward Voltage	V_{FM}	$I_F = 40\text{A}$	$T_C = 25^\circ\text{C}$	--	1.6	--	V
			$T_C = 150^\circ\text{C}$	--	1.5	--	
Reverse Recovery Time	t_{rr}		$T_C = 25^\circ\text{C}$	--	115	--	ns
			$T_C = 150^\circ\text{C}$	--	245	--	
Reverse Recovery Current	I_{rr}	$I_F = 40\text{A},$ $di/dt = 200\text{A}/\mu\text{s}$	$T_C = 25^\circ\text{C}$	--	7	--	A
			$T_C = 150^\circ\text{C}$	--	15	--	
Reverse Recovery Charge	Q_{rr}		$T_C = 25^\circ\text{C}$	--	420	--	nC
			$T_C = 150^\circ\text{C}$	--	2900	--	

IGBT Characteristics

Fig. 1 Output characteristics

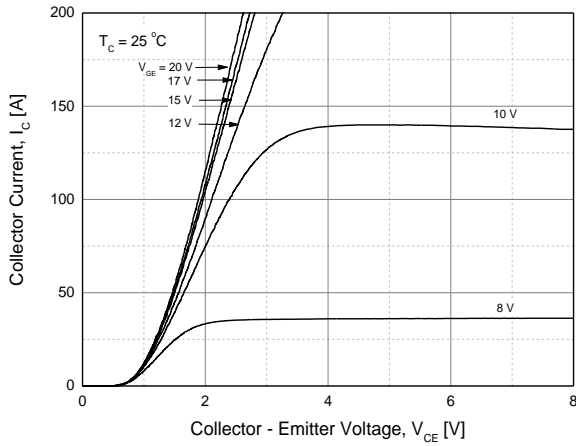


Fig. 2 Saturation voltage characteristics

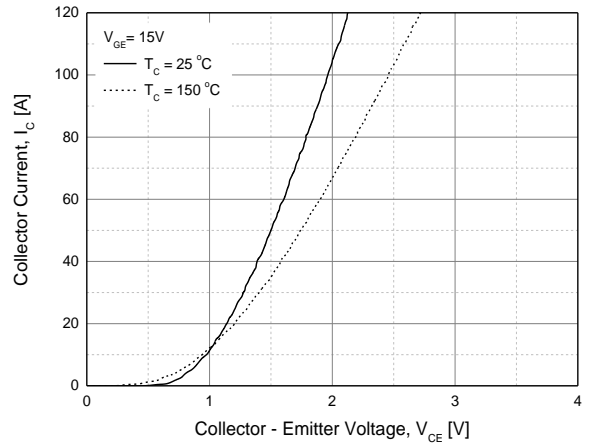


Fig. 3 Saturation voltage vs. collector current

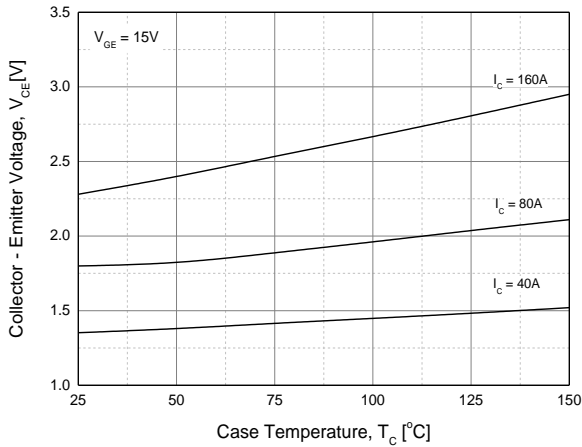


Fig. 4 Saturation voltage vs. gate bias

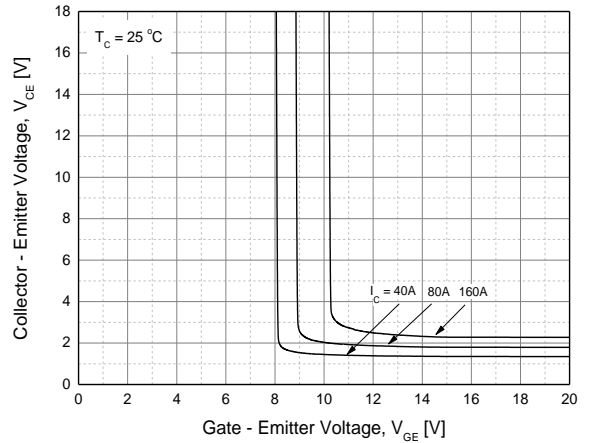


Fig. 5 Saturation voltage vs. gate bias

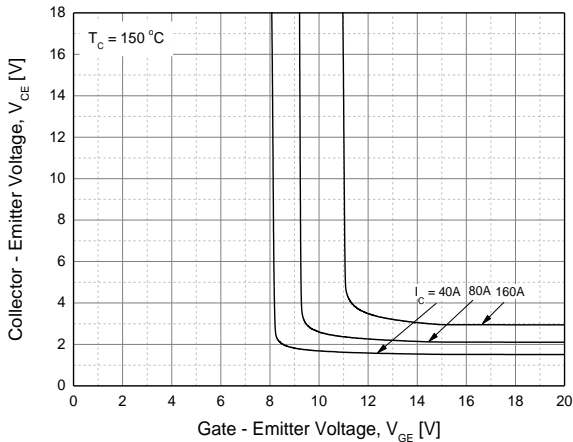
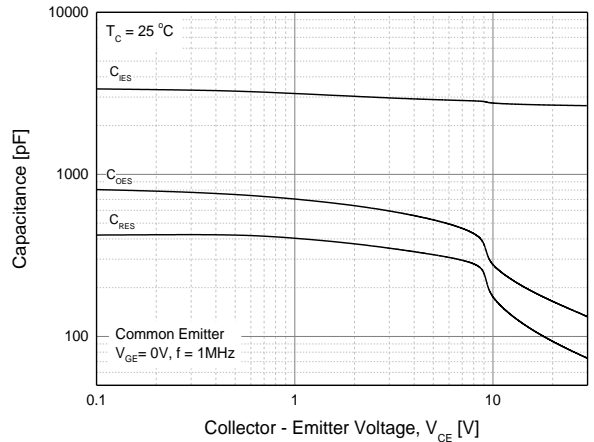


Fig. 6 Capacitance characteristics



IGBT Characteristics

Fig. 7 Turn-on time vs. gate resistor

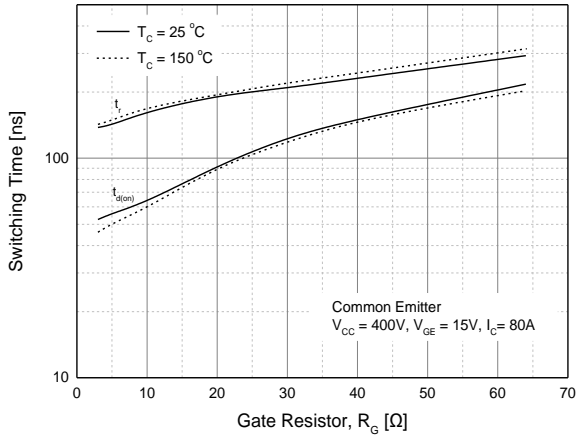


Fig. 8 Turn-off time vs. gate resistor

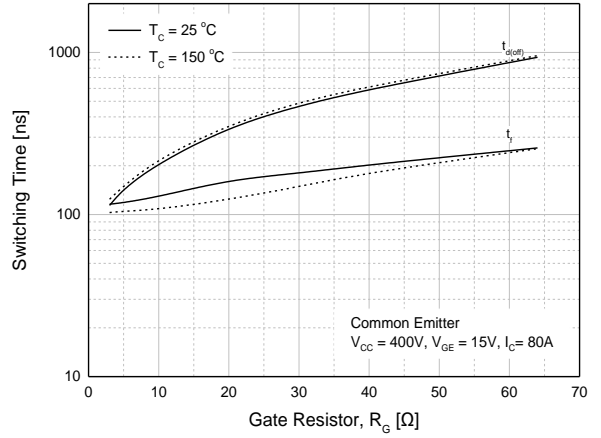


Fig. 9 Switching loss vs. gate resistor

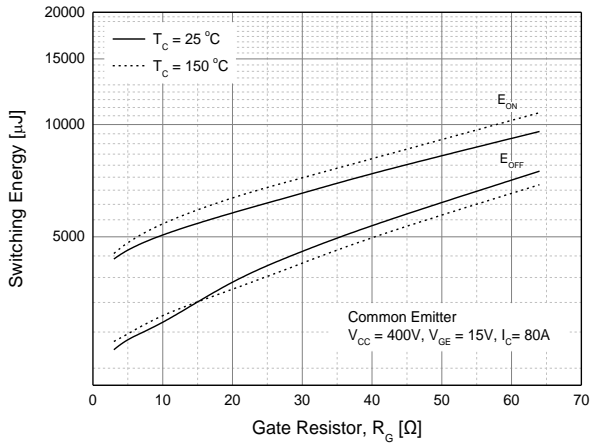


Fig. 10 Turn-on time vs. collector current

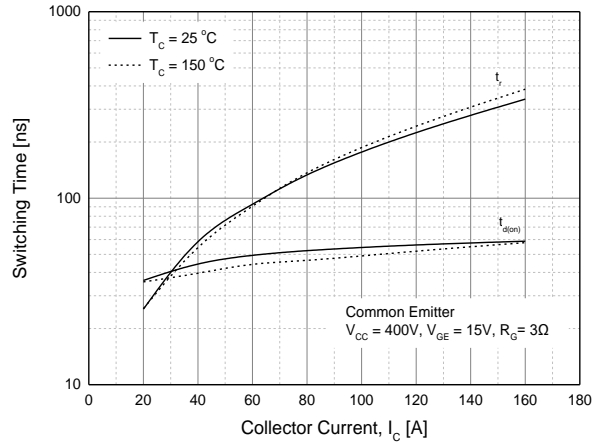


Fig. 11 Turn-off time vs. collector current

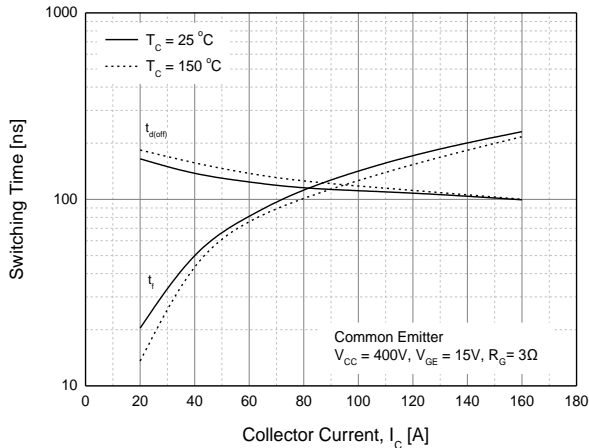
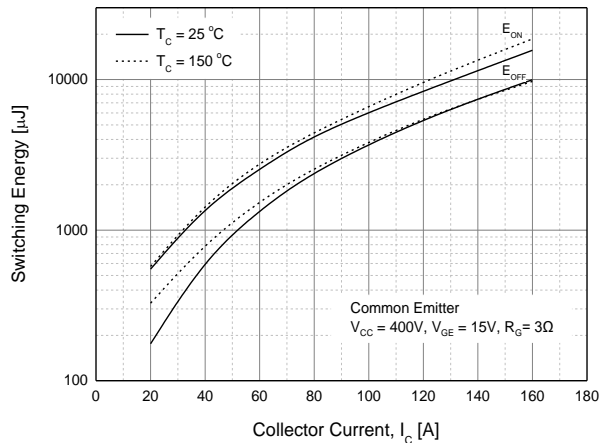


Fig. 12 Switching loss vs. collector current



IGBT Characteristics

Fig. 13 Gate charge characteristics

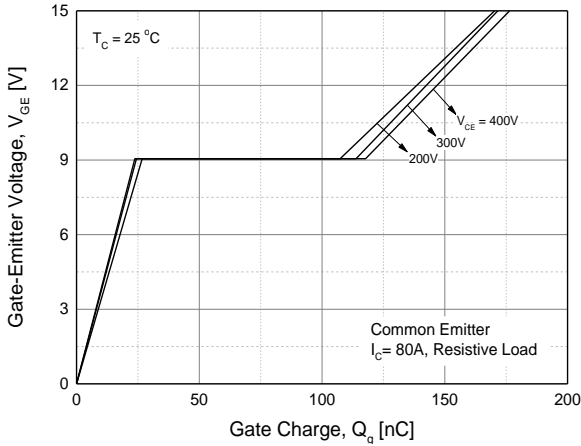


Fig. 14 SOA

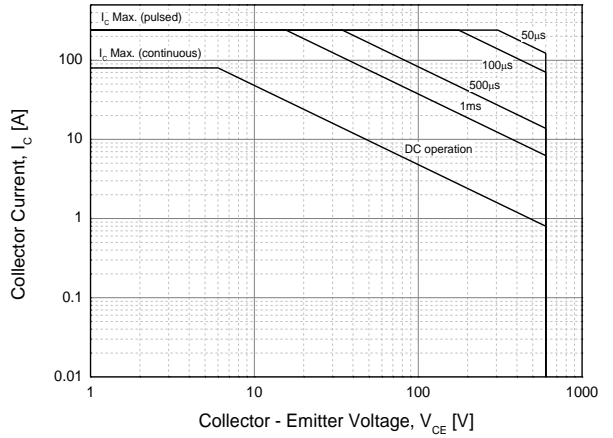


Fig. 15 RBSOA

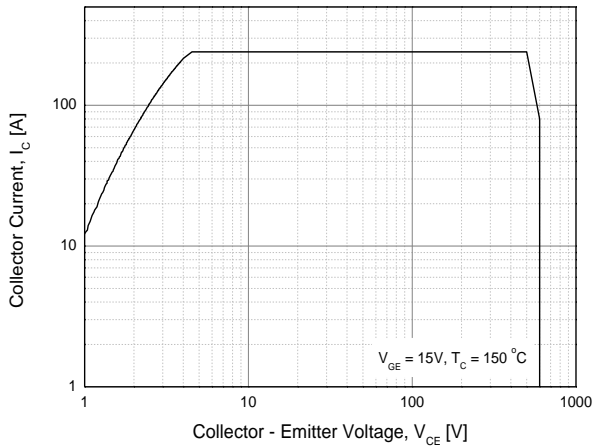


Fig. 16 Transient thermal impedance of IGBT

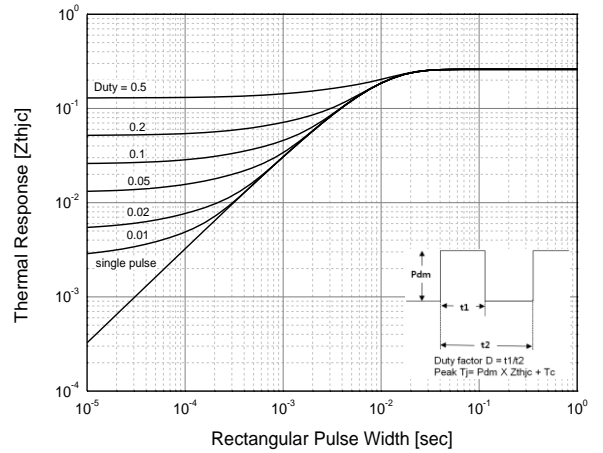
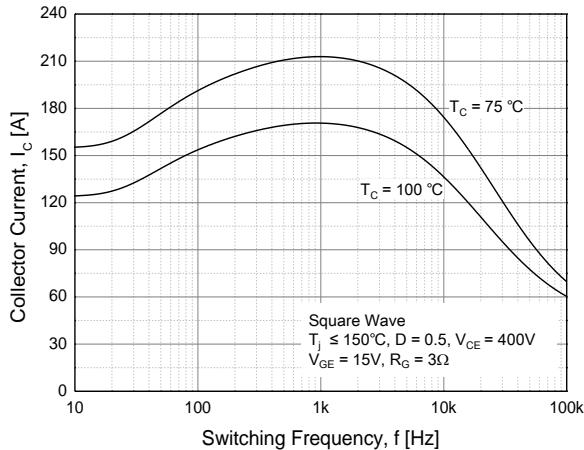


Fig. 17 Load Current vs. Frequency



Diode Characteristics

Fig. 18 Conduction characteristics

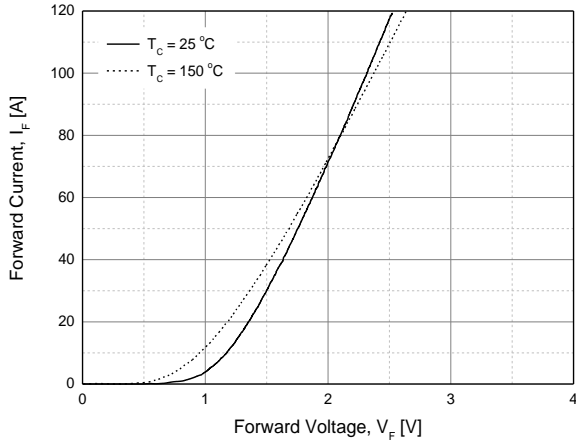


Fig. 19 Reverse recovery current vs. forward current

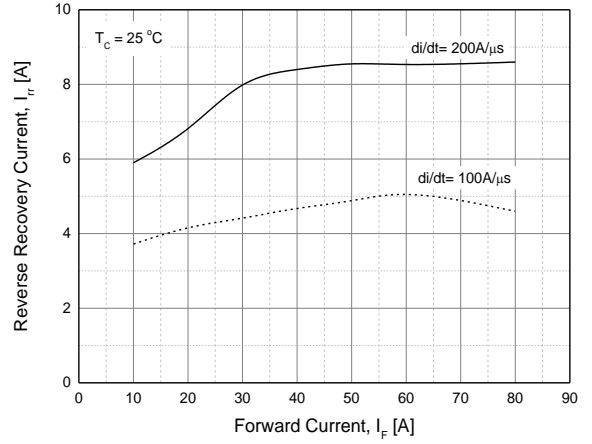


Fig. 20 Reverse recovery charge vs. forward current

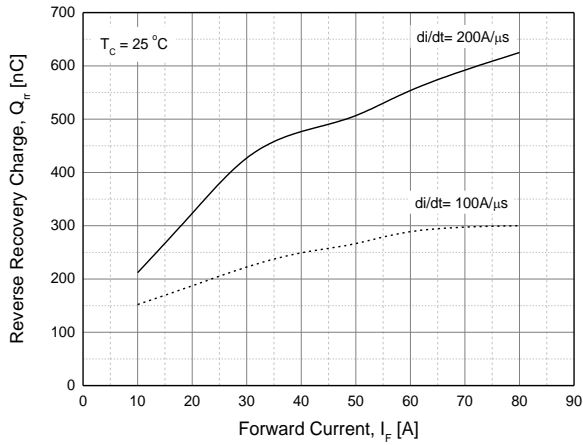
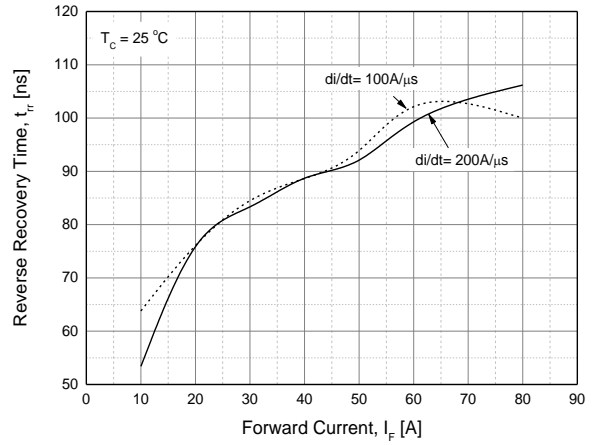
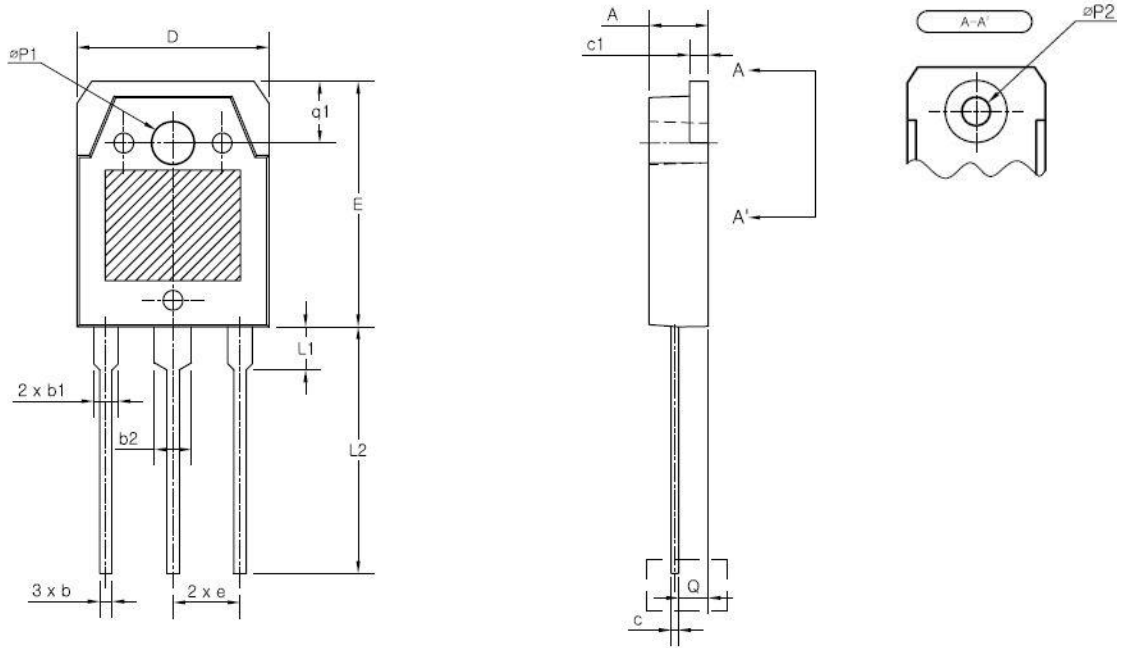


Fig. 21 Reverse recovery time vs. forward current



TO-3PN MECHANICAL DATA



SYMBOL	MIN	NOM	MAX
A	4.60	4.80	5.00
b	0.80	1.00	1.20
b1	1.80	2.00	2.20
b2	2.80	3.00	3.20
c	0.55	0.60	0.75
c1	1.45	1.50	1.65
D	15.40	15.60	15.80
E	19.70	19.90	20.10
e	5.15	5.45	5.75
L1	3.30	3.50	3.70
L2	19.80	20.00	20.20
øP1	3.30	3.40	3.50
øP2	(3.20)		
Q	2.20	2.40	2.60
q1	4.80	5.00	5.20

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